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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/701,420	01/27/2000	Katsunori Kumasaka	0694-127	9104
7.	590 11/10/2004		EXAM	INER
BRADLEY RUBEN			BUDD, MARK OSBORNE	
463 FIRST ST. SUITE 5A HOBOKEN, NJ 07030			ART UNIT	PAPER NUMBER
			2834	

DATE MAILED: 11/10/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

			<u>atr</u>			
ACTO PROPERTY.		Application No.	Applicant(s)			
Office Action Summary		09/701,420	KUMASAKA ET AL.			
		Examiner	Art Unit			
		Mark Budd	2834			
Period fo	The MAILING DATE of this communication ap or Reply	pears on the cover sheet with the	e correspondence address			
THE - Extermination of the control	ORTENED STATUTORY PERIOD FOR REPL MAILING DATE OF THIS COMMUNICATION. nsions of time may be available under the provisions of 37 CFR 1. SIX (6) MONTHS from the mailing date of this communication. e period for reply specified above is less than thirty (30) days, a repl period for reply is specified above, the maximum statutory period re to reply within the set or extended period for reply will, by statuf reply received by the Office later than three months after the mailing patent term adjustment. See 37 CFR 1.704(b).	.136(a). In no event, however, may a reply be ply within the statutory minimum of thirty (30) of d will apply and will expire SIX (6) MONTHS for te, cause the application to become ABANDO	timely filed days will be considered timely. om the mailing date of this communication. NED (35 U.S.C. § 133).			
Status						
1)⊠	Responsive to communication(s) filed on 12 (October 2004.				
·	This action is FINAL . 2b) ☐ This action is non-final.					
3)□	☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is					
	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Dispositi	ion of Claims					
5)□ 6)⊠ 7)□	Claim(s) <u>4-17</u> is/are pending in the application 4a) Of the above claim(s) is/are withdra Claim(s) is/are allowed. Claim(s) <u>4-17</u> is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restriction and/	awn from consideration.				
Applicati	ion Papers					
9)[The specification is objected to by the Examin	er.				
10)	10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.					
	Applicant may not request that any objection to the	e drawing(s) be held in abeyance. S	See 37 CFR 1.85(a).			
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).					
11)	The oath or declaration is objected to by the E	xaminer. Note the attached Office	ce Action or form PTO-152.			
Priority ι	under 35 U.S.C. § 119					
a)[Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority document 2. Certified copies of the priority document 3. Copies of the certified copies of the priority application from the International Bureasee the attached detailed Office action for a list	nts have been received. Its have been received in Application of the price of the	ation No ived in this National Stage			
Attachmen	t(s)					
	e of References Cited (PTO-892)	4) Interview Summa				
	e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08	Paper No(s)/Mail 5) Notice of Informa	Date I Patent Application (PTO-152)			
	r No(s)/Mail Date	6) Other:	•			

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Claims 4-6 are rejected under 35 USC 103 as unpatentable over the Prior Art (application's fig. 1 or Japan (250). Each reference (note fig. 5 of JP (250)) teaches the piezoelectric transformer structure with at least two pairs of output electrodes on the side surfaces (e.g. 327, 29, 331, 32, #35, 37 fig. 1 3124a, #14b, #of JP 250) the second electrodes kept at a same potential (e.g. #114a, #14b of JP250 and #43 and 47 of Prior Art fig. 1). They don't explicitly teach the transformer to be mounted on a PCB that also contains a power supply circuit. However, applicants description of the prior art (e.g. specification pg. 1-3 and Japan (033) teaches the transformer is routinely mounted onto a printed circuit board and is also routinely used with a power supply circuit. Placing both on a common PCB to save space and allow integrated circuit construction methods would have been obvious to one of ordinary skill in the art.

Claims 10, 11 are rejected under 35 USC 103 as unpatentable over Inoi. (Figs. 8 and 12) teaches the piezoelectric transformer with elastic support bodies (#7, #4(, which are located within each of areas occupying one fifth of the full length of said transformer from both ends thereof". The transformer is 42mm long (see col. 8, line 42), therefore nodes #21, #23 at one quarter the length are located at 10.5mm from each end.

Mounting member 34 is 9.5mm wide (col. 9, line 31) and lies between the nodes and the ends. Since one fifth of 42 mm is approximately 8mm, the supports 43 must occupy this designated area. In Fig. 12, supports #7 cover all of both ends and therefore also occupy the claimed mounting locations. Inoi does not explicitly show the transformer mounted directed onto a PCB with the appropriate input/output circuitry. However, as noted above such arrangements are conventional (note e.g. applicants description of

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the Prior Art and Japan (033) to thus dispose Inoi's transformer directly on a PCB, or incorporate a PCB with circuitry into Inoi's housing (e.g. to save space) would have been obvious to one of ordinary skill in the art.

Claims 7-9 and 12-17 are rejected under 35 USC 103 as unpatentable over Inoi in view of Japan (250) or the Prior Art (applicant's fig. 1) or vice versa. Inoi teaches a piezoelectric transformer supported by elastic members within one fifth of the length from each end. Inoi does not teach multiple output electrode pairs on side surfaces. However, each of the Prior Art and Japan (250), as noted above, teach this specific transformer to be well known per se. The routineer would not limit (Inoi's mounting advantages to the specific transformer illustrated, but would realize that any similar transformer could be place in Onoi's mount. (selection from among know, equivalent piezo transformers. Thus to mount either the specific Prior Art (applicants fig. 1) or Japan (250) transformer within Ono's mounting would have been obvious to one of ordinary skill in the art. Conversely, to select from among known piezoelectric transformer mountings for the Prior Art and Japan (250) and use the mount of Inoi (e.g. due to its superior protection via elastic mounts) would have been obvious to one of ordinary skill in the art.

Regarding applicants comments, it is noted that Japan (250) and Inoi remain valid references since applicant has not supplied a sworn English language translation of priority document JP 149660/1998.

The examiner acknowledges that patent drawings are not necessarily to scale.

The examiner disagrees that a relationship between a node and mounting location does

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not also disclose the relationship between the mounting location and an end of the transformer sine the relationship between transformer length and nodes is fixed (known) depending on the mode of operation. Whether or not the 9.5mm dimension is an error is pure speculation.

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Budd/ds

11/03/04

RIMARY EXAMINER